Form PTO-1449 (modified) List of Patents and Publications for Applicant's Information Disclosure Statement (Use several sheets if necessary)	ATTY. DKT. NO. 5580-04401 APPLICANT: Yeh, et al. FILING DATE: February 4, 2002	SERIAL NO. 10/066,941 GROUP: 2183

U.S. PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TM	B1	6,076,153	6/13/00	Grochowski, et al.			
					RECEIVED		
					NOV 2 0 2002		
					Technology Center 2100		

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

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Information Disclosure Statement--PTO 1449 (modified)

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EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
TM	A1	5,630,157	5/13/97	Dwyer, III			
TM	A2	6,195,744	2/27/01	Favor, et al.			
TM	A3	5,887,185	3/23/99	Lynch			
TM	A4	5,673,426	9/30/97	Shen, et al.			
TM	A5	5,933,618	8/3/99	Tran, et al.			
TM	A6	5,991,884	11/23/99	Lin, et al.			
TM	A7	6,282,663	8/28/01	Khazam			
TM	A8	6,219,796	4/17/01	Bartley			
TM	A9	5,666,506	9/9/97	Hesson, et al.			
TM	A10	5,655,096	8/5/97	Branigin			
TM	A11	5,625,837	4/29/97	Popescu, et al.			
TM	A12	5,546,593	8/13/96	Kimura, et al.			
TM	A13	5,497,499	3/5/96	Garg, et al.			
TM	A14	5,488,730	1/30/96	Brown, III, et al.			
TM	A15	5,487,156	1/23/96	Popescu, et al.			
TM	A16	5,790,827	8/4/98	Leung			
TM	A17	5,784,588	7/21/98	Leung			

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TM ✓	A18	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Morgan Kaufmann Publishers, Inc., 1990, pages 291-299.
TM ✓	A19	Patterson and Hennessy, "Computer Architecture: A Quantitative Approach," Second Edition, Morgan Kaufmann Publishers, Inc., 1990, 1996, pages 242-251.
TM ✓	A20	SiByte, "Target Applications," http://sibyte.com/mercurian/applications.htm, January 15, 2001, 2 pages.
TM ✓	A21	SiByte, "SiByte Technology," http://sibyte.com/mercurian/technology.htm, January 15, 2001, 3 pages.
TM ✓	A22	SiByte, "The Mercurian Processor," http://sibyte.com/mercurian, January 15, 2001, 2 pages.
TM ✓	A23	SiByte, "Fact Sheet," SB-1 CPU, October 2000, rev. 0.1, 1 page.
TM ✓	A24	SiByte, "Fact Sheet," SB-1250, October 2000, rev. 0.2, 10 pages.

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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION YES/NO

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TM✓	A25	Stepanian, SiByte, "SiByte SB-1 MIPS64 CPU Core, Embedded Processor Forum 2000, June 13, 2000, 15 pages.
TM✓	A26	Jim Keller, "The Mercurian Processor: A High Performance, Power-Efficient CMP for Networking," October 10, 2000, 22 pages.
TM✓	A27	Tom R. Halfhill, "SiByte Reveals 64-Bit Core For NPUs; Independent MIPS64 Design Combines Low Power, High Performance," Microdesign Resources, June 2000, Microprocessor Report, 4 pages.
TM✓	A28	SiByte, Letter from Anu Sundaresan, May 18, 2000, 1 page.

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